

[POWER SOURCE DETECTOR]

Abstract

A power detector and a power detecting method are presented in the embodiment of the present invention. Equipped with the power detector or exploiting the power detecting method, an integrated circuit (IC) will be able to automatically self-configure a data power level of the IC effectively and inexpensively. The embodiment of the present invention is particularly useful to a Dynamic Random Access Memory (DRAM) data power auto-configuration so that a data power level of the DRAM need not to be preset during a DRAM fabrication process.